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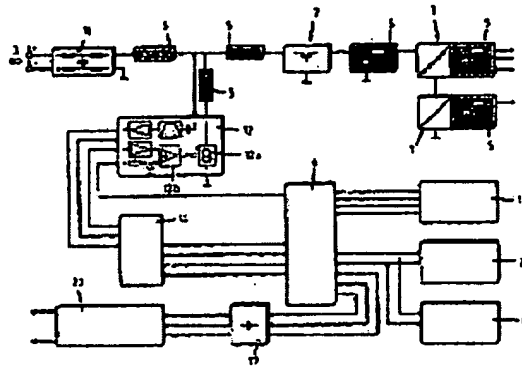
EP0906595 (A1)  
DE19646219 (A1)  
EP0906595 (B1)

**Cited documents:**

EP0654742  
EP0540903  
EP0355532  
EP0301736  
DE4434553  
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Abstract not available for WO9749017  
Abstract of correspondent: DE19646219

The object of the invention is to improve a circuit enabling external devices to communicate with a central/decentralised data processing system by means of a bus, so that different external devices may be connected to any busses. For that purpose, the circuit for connecting any external devices by means of any bus systems to the data processing system has automatic bus recognition and adapting circuits for automatically recognising the bus system required by the external device and for automatically adapting to said bus system.



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